

**PATENT APPLICATION**

Attorney Docket: 54486-D1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Parti  
Serial No.: 10/658,132  
Filed: 9/8/2003  
For: Interlocking Conductor Method for  
Bonding Wafers to Produce Stacked  
Integrated Circuits  
Group Art Unit: 2823

Examiner: Toledo, Fernando

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF AMENDMENT UNDER RULE 312**

In response to the Notice of Allowance dated 6/2/2004, Applicants submit herewith an Amendment under Rule 312 in the above identified patent application.

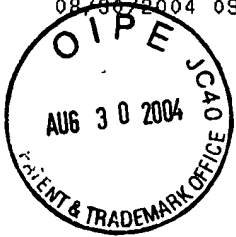
Applicants believe that no fee is required for this amendment. However, the Commissioner is hereby authorized to charge any fees which may be required in this application under 37 C.F.R. Section 1.16-1.17 during its entire pendency, or credit any overpayment, to Deposit Account No. 23-0424.

This sheet is filed in duplicate via fax number: 703-~~872-9306~~ 746-4000

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8/30/04

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**AMENDMENT UNDER RULE 312**

Commissioner for Patents  
Mail Stop:  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Under the provisions of Rule 312 of the Patent and Trademark Office (37 CFR 1.312), it is respectfully requested that the above-identified application be amended as follows: